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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10080396	FILING DATE 02/22/2002	CLASS 365	SUBCLASS 173	GAU 2818	EXAMINER Y04A
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**APPLICANTS: Shi Xizeng; Tong Hua-Ching; Menon Aric;

**CONTINUING DATA VERIFIED:
THIS APPLN CLAIMS BENEFIT OF 60/271,207 02/23/2001

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO. RR1724/2344P
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no			
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no			
Verified and Acknowledged Examiners's initials <i>Y04A</i>			
TITLE : High capacity MRAM memory array architecture			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
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